



Form PTO-1449 (modified)	Atty. Docket No. 2008.007700/03-0457	Serial No. 10/623,959
List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Applicant(s) Feng Lin	
	Filing Date: July 21, 2003	Group: 2817
U.S. Patent Documents <i>See Page 1</i>	Foreign Patent Documents <i>See Page</i>	Other Art <i>See Page 1</i>

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
<i>PL</i>	A1	5,970,106	10/19/1999	Izumikawa			
<i>PL</i>	A2	5,844,954	12/01/1998	Casasanta et al.			
<i>PL</i>	A3	5,544,203	08/06/1996	Casasanta et al.			
	A4						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						
	B3						
	B4						
	B5						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
<i>PL</i>	C1	Lin et al., <i>Phase Detector for All-Digital Phase Locked and Delay Locked Loops</i> , U.S. Patent Application Serial No. 09/652,364 filed on August 31, 2000, 22 pages.
	C2	
	C3	

EXAMINER:

Joseph Chang

DATE CONSIDERED:

12/8/04

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